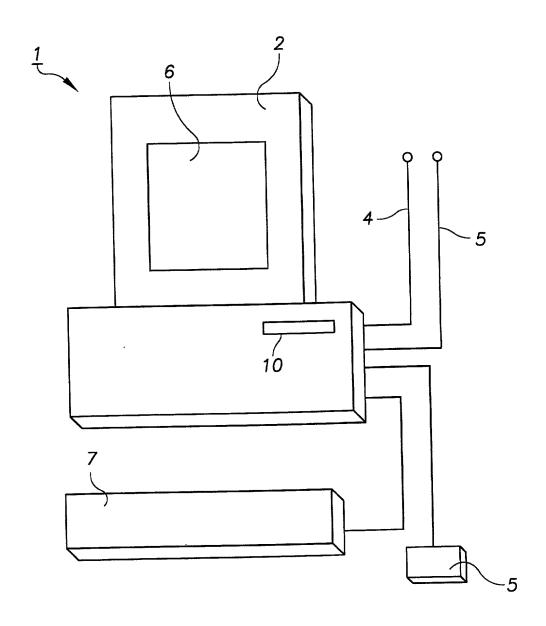
FIG.1



-

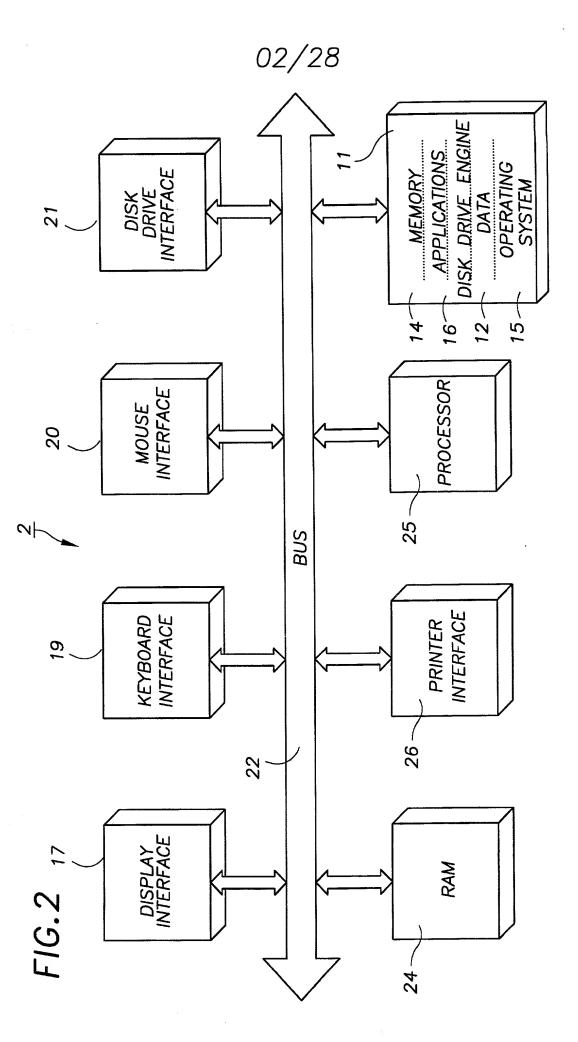
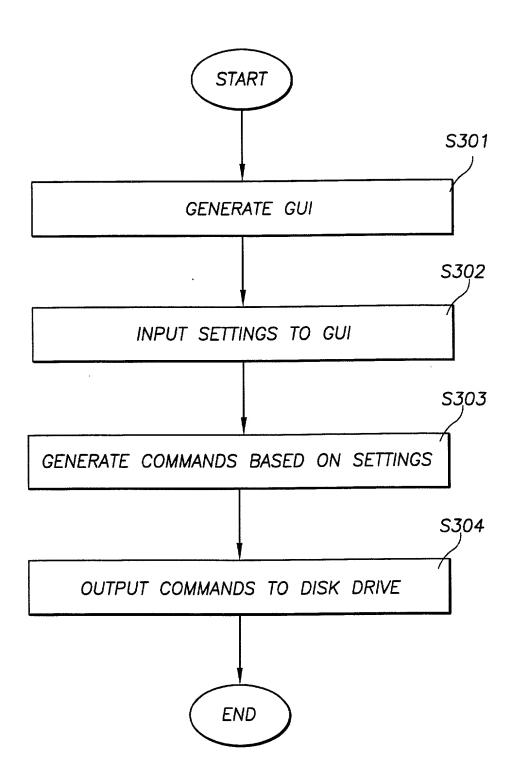
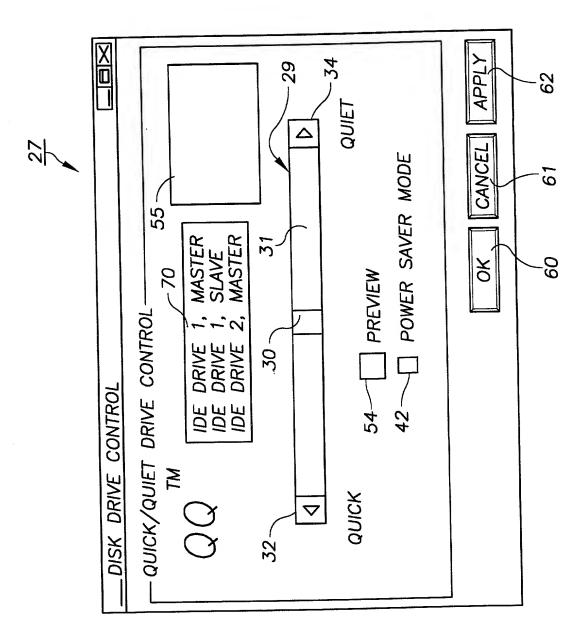
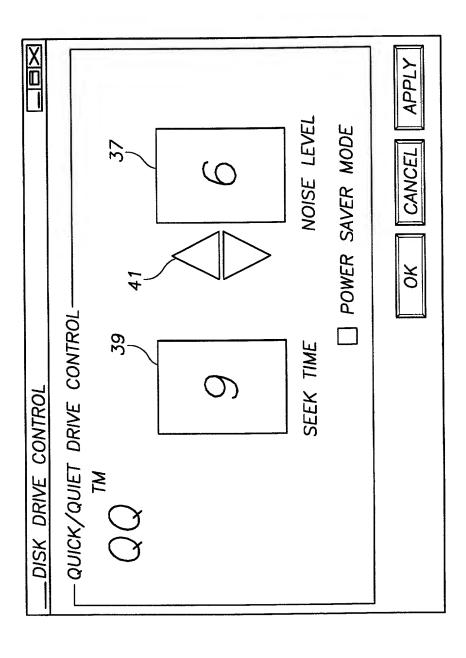


FIG.3

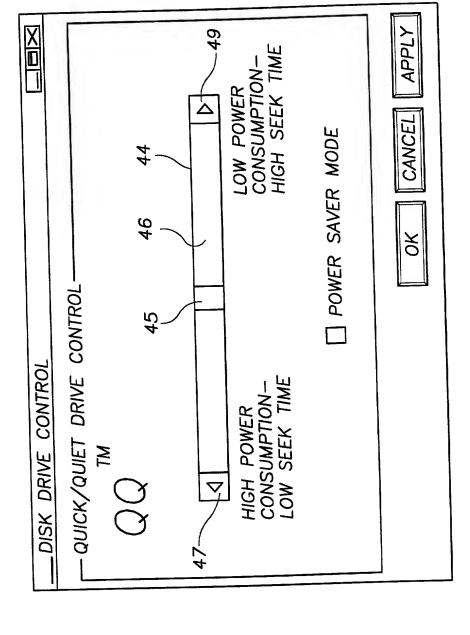




F1G.4



F1G.5



F1G.6

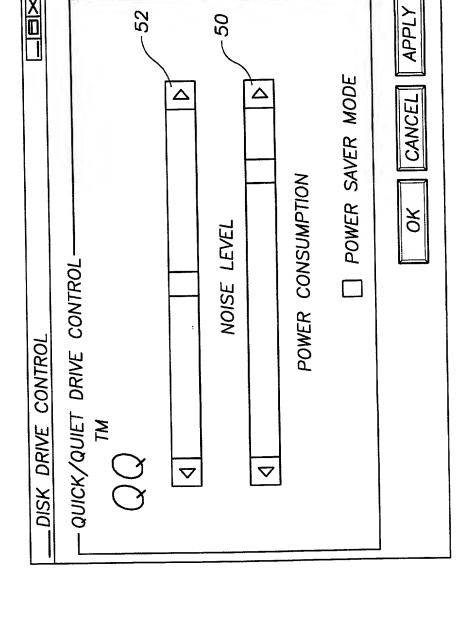
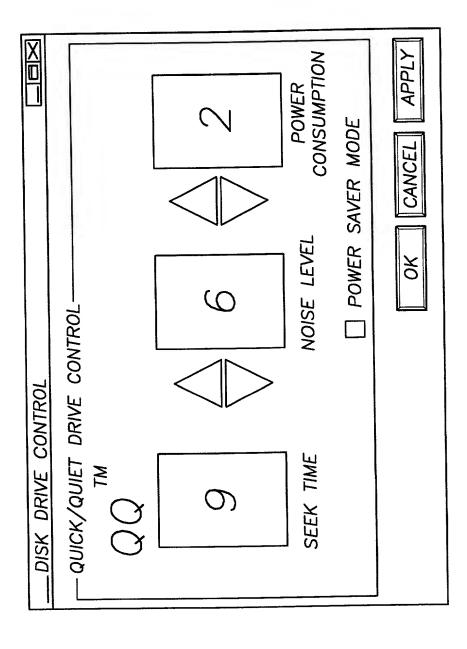


FIG.7



F1G.8

F1G.9

IDE DRIVE 1, MASTER

IDE DRIVE 1, SLAVÉ

IDE DRIVE 2, MASTER

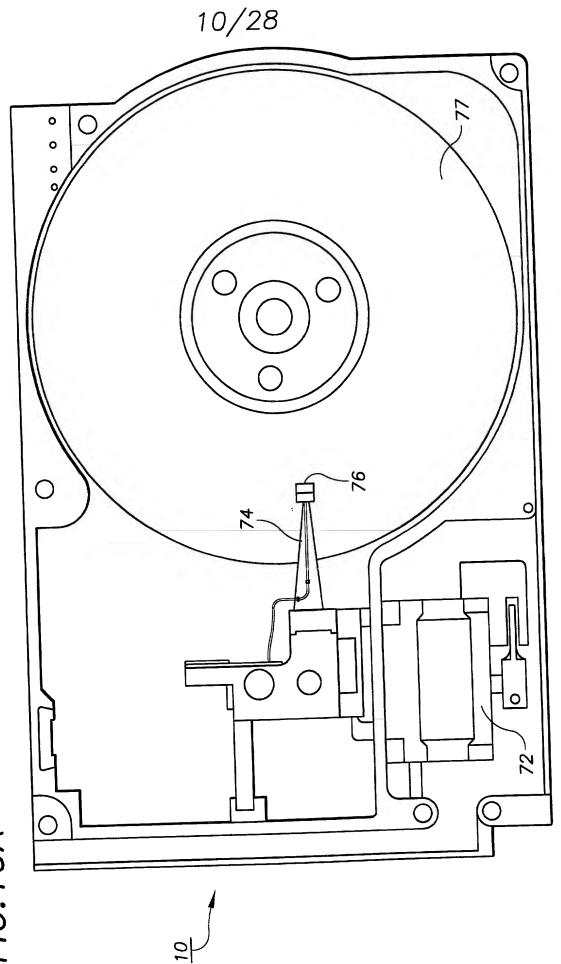
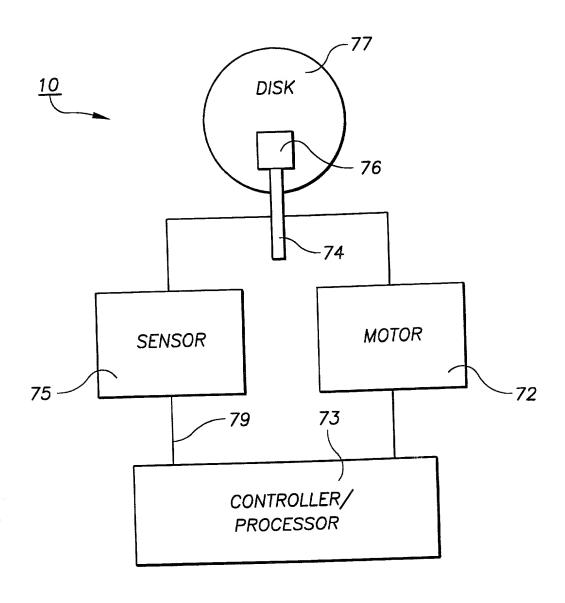
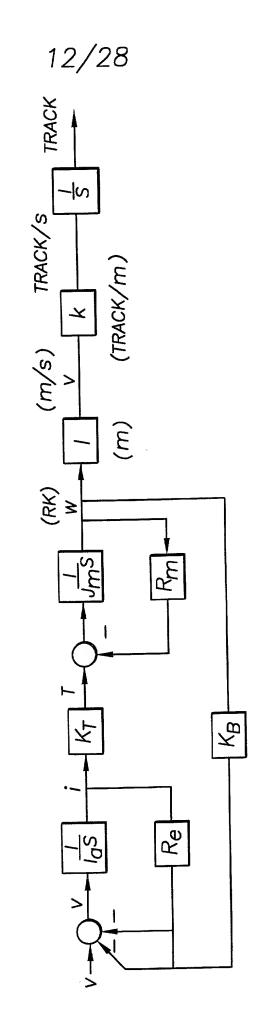
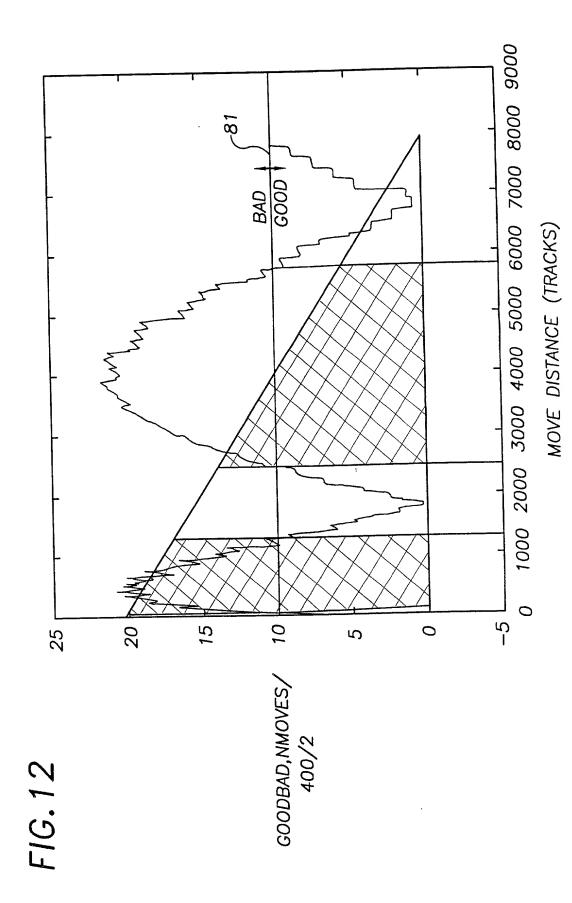


FIG. 10A







## FIG. 13

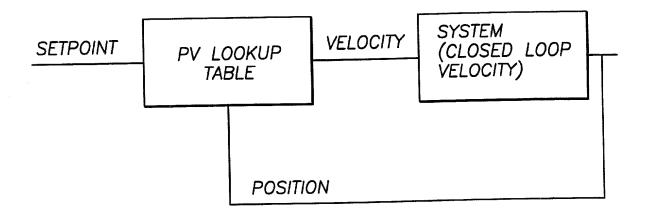


FIG. 14

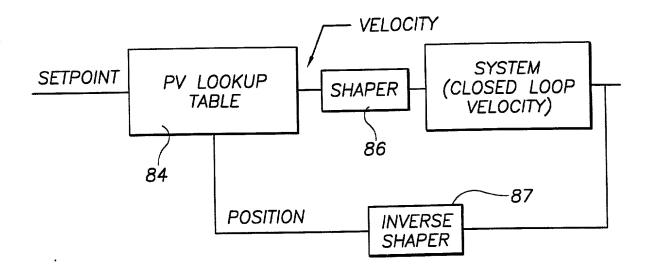


FIG. 15

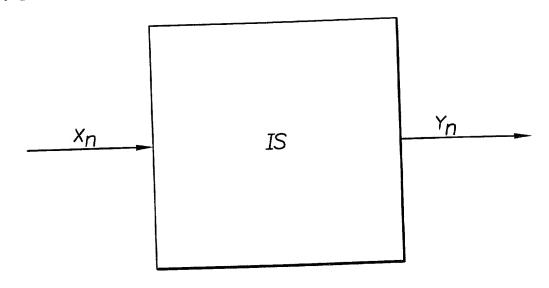


FIG. 16

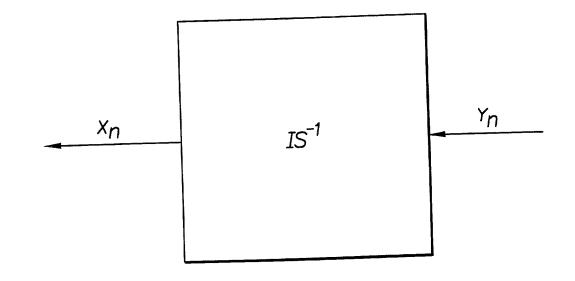


FIG. 17

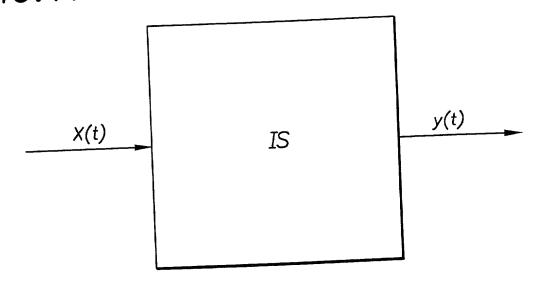


FIG18

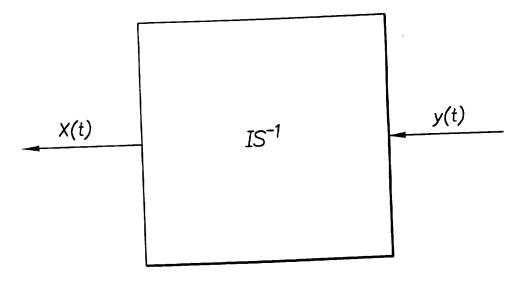


FIG.19

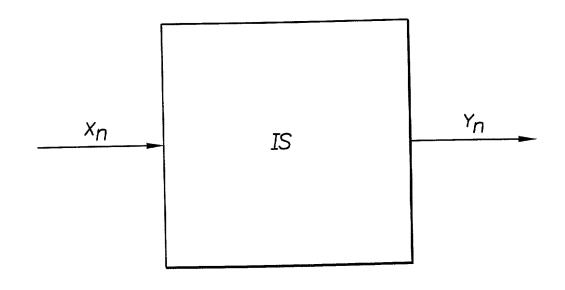
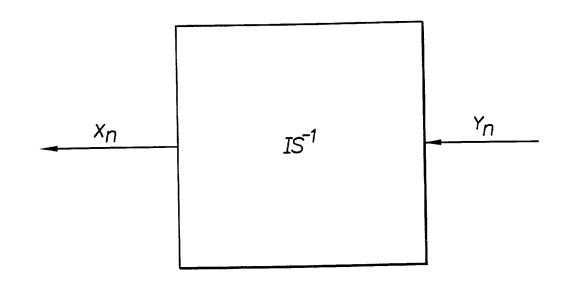
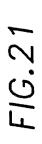
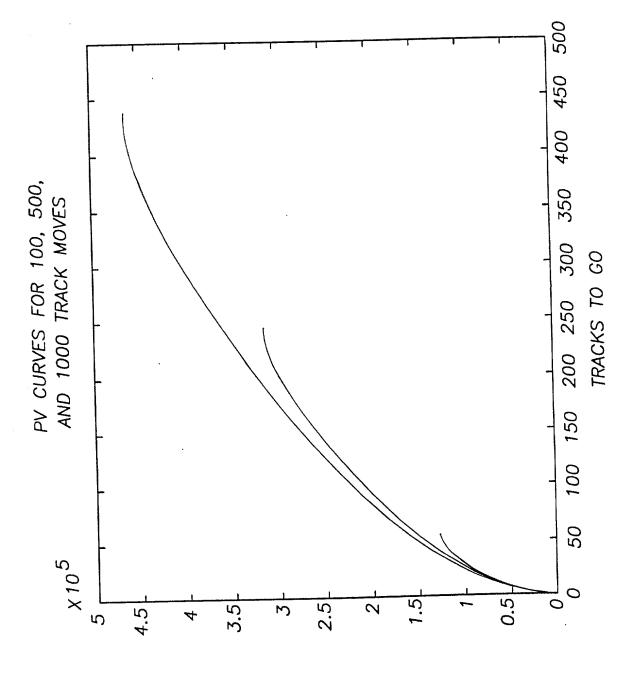


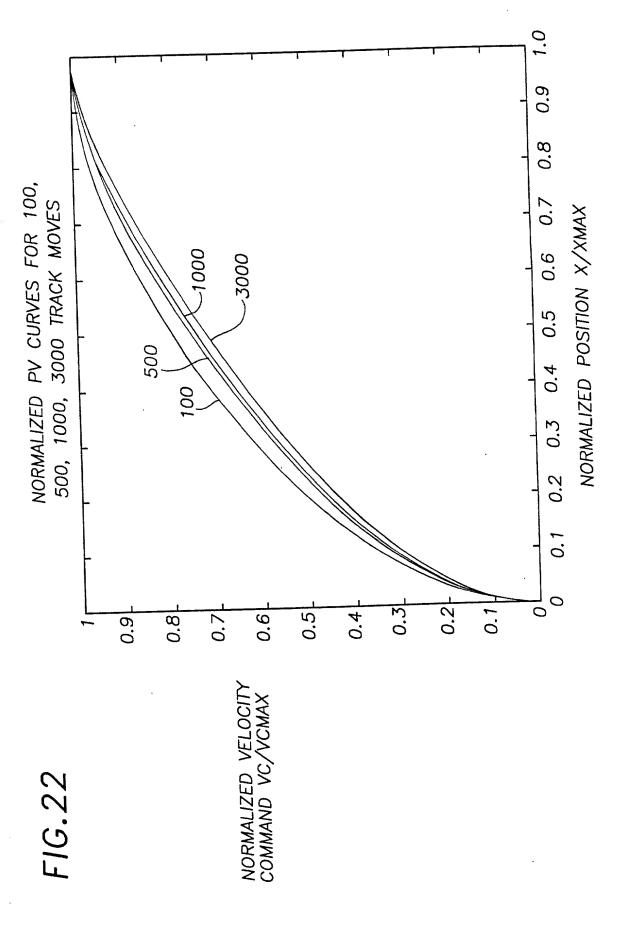
FIG20



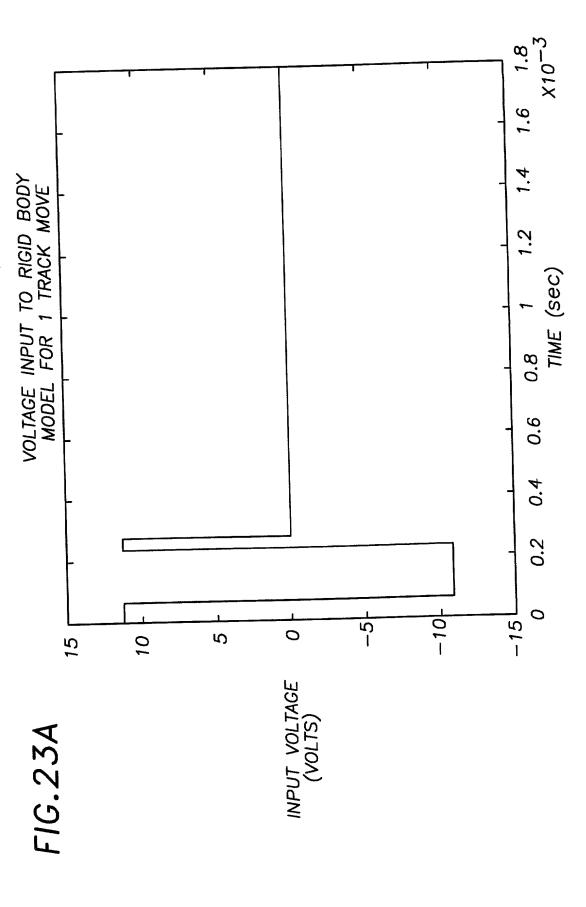


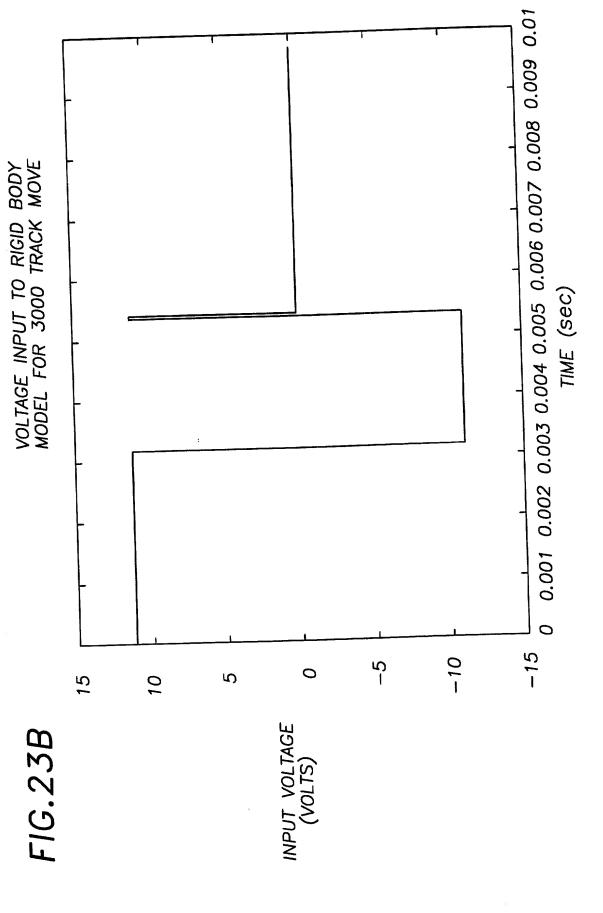


VELOCITY COMMAND (TRACKS/SEC)









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FIG.24

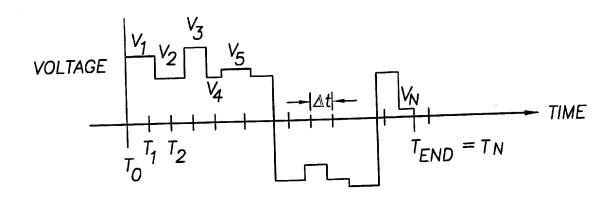


FIG.25

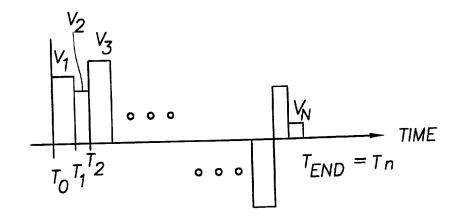
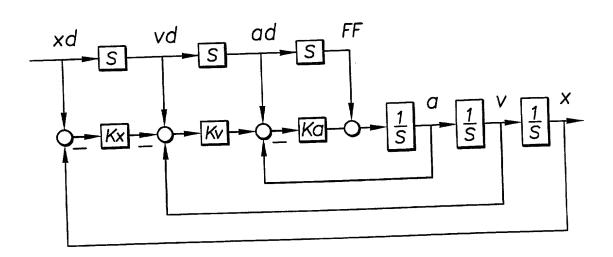
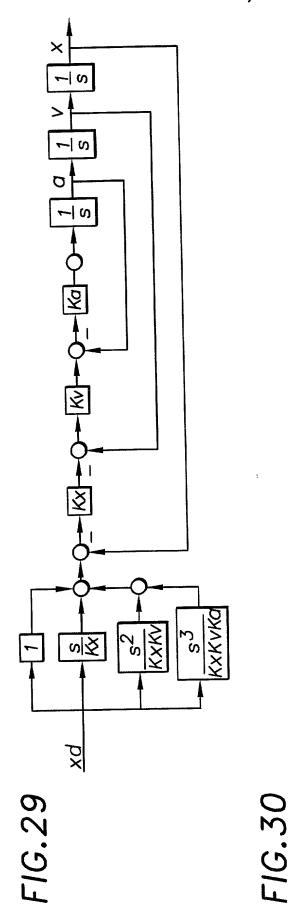


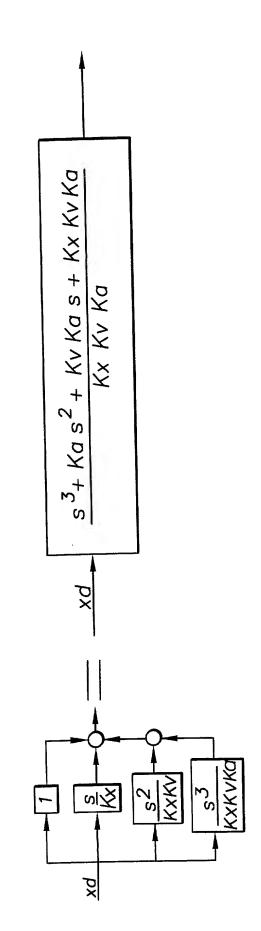
FIG.26

$$\begin{array}{c|c} V_1 & V_2 & V_N \\ \hline \hline T_1 & T_2 & + \circ \circ \circ + & \hline T_{END} \\ \hline T_0 & -V_1 & -V_2 & -V_N \\ \hline \end{array}$$

FIG.28







F1G.31

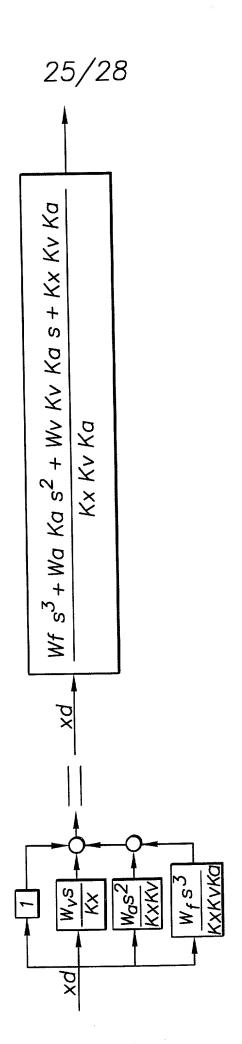
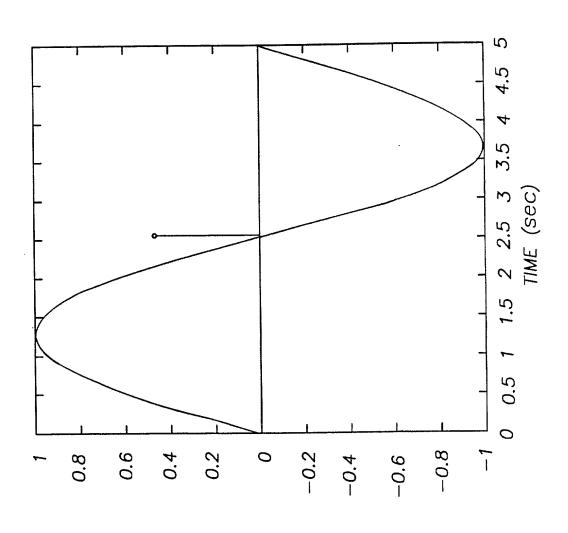


FIG.32



**AMPLITUDE** 

FIG.33

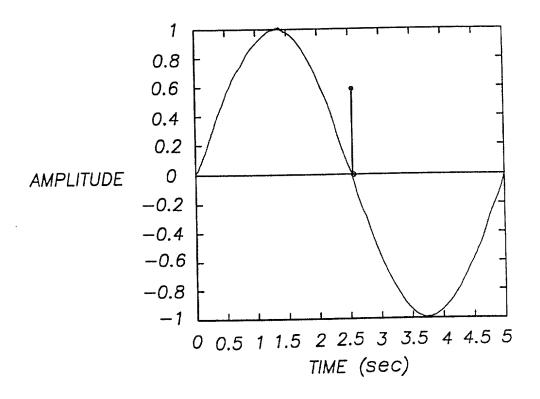


FIG.34

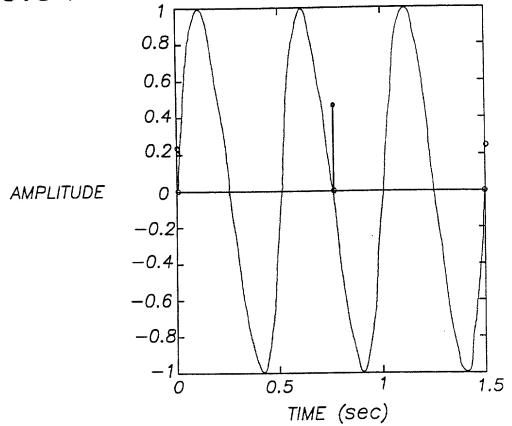


FIG.35

